Performance Analysis of Large Receive Offload in a Xen Virtualized System

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Outline

- Performance Interference in Consolidated System
- Objectives and Methodologies
- Large Receive Offload (LRO)
- Xen Internal Network Architecture
- Experimental Results
- Conclusions and Future Work

Performance Interference due to Consolidation

- Xen provides multiple computing environments (*domains*).
- Each domain looks as a logically independent machine: (runs different instances of OS, with different policies, etc).
- Domains share system resources and better utilization is expected by consolidation.
- Conflicting resource demands from domains are controlled by Xen, in a better way than a shared server, but not perfectly – leading to performance interference among domains.

Objectives and Methodologies

- Run two domains on a Xen virtualized system and analyze the performance interference between domains.
- One domain runs a CPU-intensive workload (SPECjbb), a Java-implementation of TPC-C with varying CPU utilization.
- Another domain runs **netserver**, the receiver part of the **netperf** network instrumentation suite. We measure how workloads of these domains interfere each other.
- Large receive offload, an optimization for network throughput, is ported to Xen. We see how it affects the standalone and consolidated performance.





LRO has been ported to Physical and Virtual NICs in Xen (pLRO and vLRO).



Benchmarking Environments

Component	Description
CPU	Xeon 3GHz
Memory	$2\mathrm{GB}$
NIC	$1\mathrm{Gbps}$
Operating System	Linux 2.6.18
VMM	Xen 3.1.1
Network Measurement	Netperf 2.4.4
Java workload	SPECjbb2001 v1.04
Guest Domains	
vCPU	1/domain
Memory	512MB/domain







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Conclusions and Future Work

- Ported LRO into NICs of Xen and evaluated its effectiveness under varying MTU and message length.
- Run two guest domains on Xen, one with SPECjbb and the other with network receiver.
- Evaluated performance interference between two domains and effectiveness of LRO in this environment.
- Topics of future work include: (1) LRO optimization,
 (2) further investigation and modeling of performance interference.

